

Abstract

The invention relates to a circuit configuration for determining the average value of an input signal (s), comprising a signal input (1) for receiving the input signal (s) and comprising a signal output (13) for outputting an output signal (g) that depicts the average value of the input signal (s). According to the invention, a counter (10) is placed between the signal input (1) and the signal output (13) in order to conduct an averaging. Said counter is preferably controlled by a sigma-delta modulator (2).